



## PRESS RELEASE

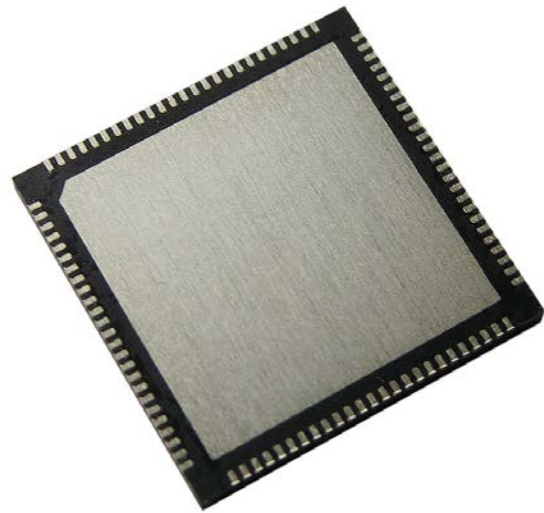
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### TopLine Introduces Large Scale QFN Daisy Chain Test Package

Irvine California, USA – TopLine now produces very large size 12x12mm Daisy Chain QFN components with dummy silicon die for use in a variety of SMT process and thermal profile testing applications. These large-size, bottom terminal QFN packages feature 100 pins, spaced very fine pitch at 0.4mm. The package size conforms to JEDEC standards with package height not exceeding 1.0mm. The terminals and ground pads are plated with Nickel Palladium Gold (Ni Pd Au) to optimize SMT soldering.

The package level Daisy Chain circuit, made with 25µm gold bonding wire, forms an electrical connection between pads. These zero-ohm connections may be used to monitor electrical opens and shorts so that one may forensically determine the cause of PCB circuit board failure with simple GO/No-Go techniques using an ohm meter. Operating temperature range from -65°C to +150°C.



Other applications include PCB circuit board cleanliness testing incorporating S.I.R. Surface Insulation Resistance monitoring. For more information, visit [www.topline.tv/DaisyChain.html](http://www.topline.tv/DaisyChain.html)

#### About TopLine

TopLine manufactures a wide range of daisy chain semiconductor packages for process development, experimentation, machine evaluation, solder training, and SMT assembly practice. TopLine products provide hands-on learning for engineers. TopLine is a one-stop source for all dummy components. Contact TopLine Corporation, Tel (800) 776-9888; Email: [info@TopLine.tv](mailto:info@TopLine.tv).

**Photo Caption:** TopLine Very Large Size QFN 100 (12mm). Photo by TopLine's Danielle Davey.

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